Class AB Stereo Headphone Driver, Low voltage Volume Controller -79~+15dB With Soft Step Input Gain 0~+15dB

FEATURES

- Operation range: 2.5V~6.5V.
- Soft step volume control：-79dB ~ +15dB.
- Input Gain: 0dB ~ +15dB.
- Low power consumption.
- Good PSRR and low pop noise.
- I^2C interface.
- Housed in 10 pin MSOP package.

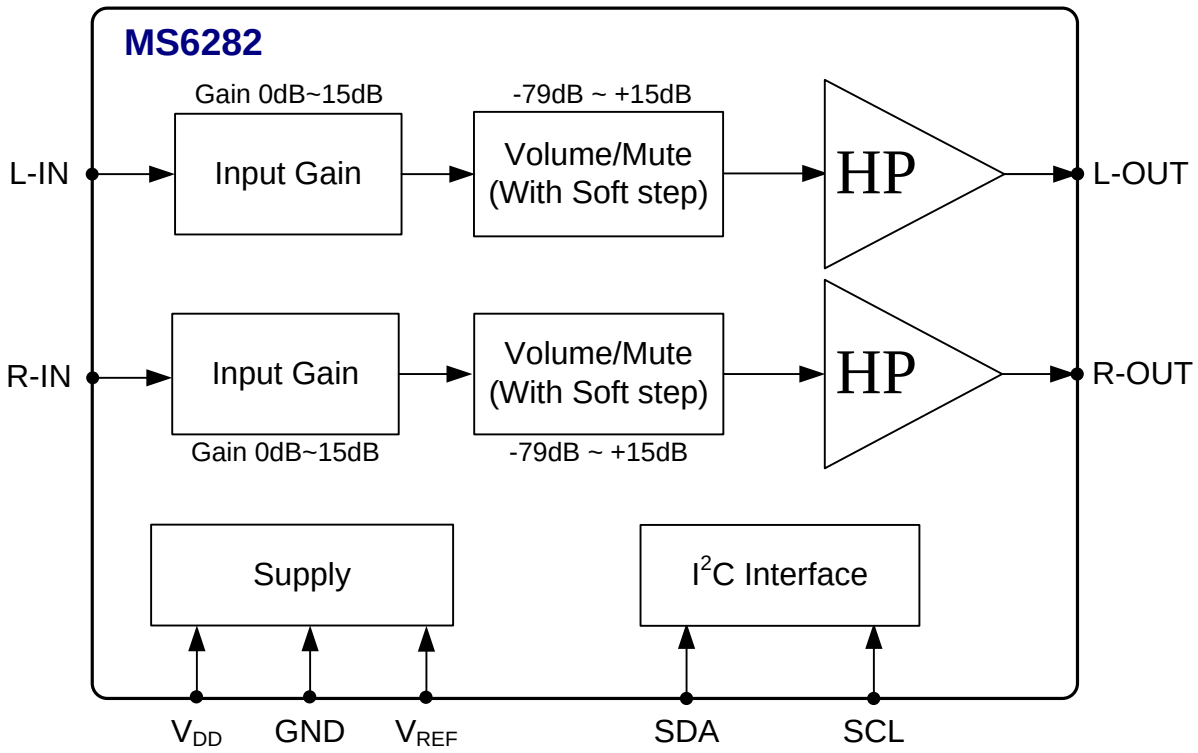
APPLICATIONS

- Multimedia system.
- Hi-Fi audio system.
- Bluetooth.
- DAB

DESCRIPTION

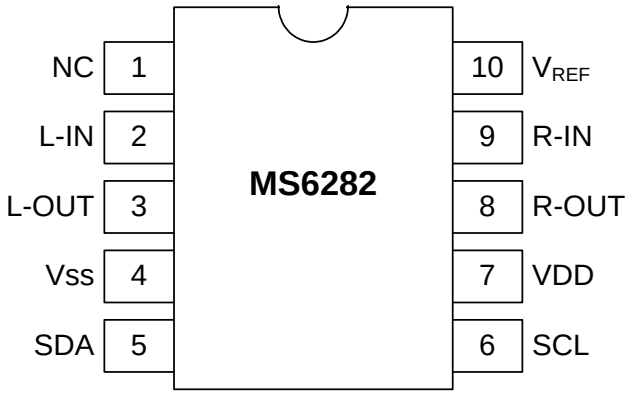
The MS6282 is a class AB headphone drivers with audio volume controller, It has Input gain(0 ~ +15dB), Soft step volume control(-79dB ~ +15dB) with programmable blend times.
It uses CMOS technology specially for the low voltage application with low noise, rail-to-rail output.

BLOCK DIAGRAM



PIN CONFIGURATION

Symbol	Pin	Description
NC	1	No Connected
L-IN	2	Left Channel Input
L-OUT	3	Left Channel Output
V_{SS}	4	Ground
SDA	5	I^2C Data Input
SCL	6	I^2C Clock Input
V_{DD}	7	Positive Supply Voltage
R-OUT	8	Right Channel Output
R-IN	9	Right Channel Input
V_{REF}	10	Reference Voltage = $1/2V_{DD}$



ORDERING INFORMATION

Package	Part number	Packaging Marking	Transport Media
10-Pin MSOP (lead free)	MS6282MGTR	6282G	3.5k Units Tape and Reel
10-Pin MSOP (lead free)	MS6282MGU	6282G	80 Units Tube

RoHS Compliance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
VDD	Supply Voltage	6.5	V
V_{ESD}	Electrostatic Handling	-3000 to 3000	V
T_{STG}	Storage Temperature Range	-65 to 150	℃
T_A	Operating Ambient Temperature Range	-40 to 85	℃
T_J	Maximum Junction Temperature	120	℃
T_S	Soldering Temperature, 10 seconds	260	℃
R_{THJA}	Thermal Resistance from Junction to Ambient in Free Air MSOP10	165.9	℃/W

OPERATING RATINGS

Symbol	Parameter	Min	Typ	Max	Unit
V_{DD}	Supply Voltage	2.5	-	6.5	V

5V ELECTRICAL CHARACTERISTICS

(Ta=25℃;V_{DD}=5V, V_{SS}=0V; C_{REF} = 1uF ; R_L=32 Ω ; refer to the application circuit; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply						
I_Q	Quiescent Current	V_{IN}=0V	-	6	-	mA
I_{PD}	Power down current	V_{IN}=0V	-	130	-	uA
PSRR	Power Supply Rejection Ratio	f = 100Hz	55	58	-	dB
Input Selectors						
R_{IN}	Input Resistance			100		kΩ
G_{IN}	Input Gain Range	Gain	0	-	15	dB
G_{STEP}	Step Resolution		-	1	-	dB
ERR_G	Gain Setting error		-0.2	0	0.2	dB
Volume control						
CR_{VOL}	Volume Control Range	Attenuation & Gain	-79	-	+15	dB
RES_{VOL}	Volume Step Resolution		-	1	-	dB
ERR_{VOL}	Volume Setting Error	Av = +15 to -40dB	-0.5	0	1	dB
		Av = -40 to -79dB	-1	0	5	dB
MUTE	Mute Attenuation	Vin=0dBV		-90		dB
General						
VO_{MAX}	Maximum Output Voltage Swing	(THD+N)/S <0.1%	-	1.45	-	Vrms
THD+N	Total Harmonic Distortion Plus Noise	V_{OUT} = 1Vrms	-	-64	-	dB
			-	0.063	-	%
S/N	Signal-to-Noise Ratio	V_{OUT} = 1Vrms	-	93	-	dB
Bus Input						
V_{IH}	Bus High Input Level		1.8	-	-	V
V_{IL}	Bus Low Input Level		-	-	0.8	V

3.3V ELECTRICAL CHARACTERISTICS

(Ta=25℃;V_{DD}=3.3V, V_{SS}=0V; C_{REF} = 1uF ; R_L=32 Ω ; refer to the application circuit; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply						
I_Q	Quiescent Current	V_{IN}=0V	-	5.4	-	mA
I_{PD}	Power down current	V_{IN}=0V	-	82	-	uA
PSRR	Power Supply Rejection Ratio	f = 100Hz	65	70	-	dB
General						
V_{OMAX}	Maximum Output Voltage Swing	(THD+N)/S <0.1%	-	1	-	Vrms
THD+N	Total Harmonic Distortion Plus Noise	V_{OUT} = 0.707Vrms	-	-65	-	dB
			-	0.056	-	%
S/N	Signal-to-Noise Ratio	V_{OUT} = 0.707Vrms	-	90	-	dB

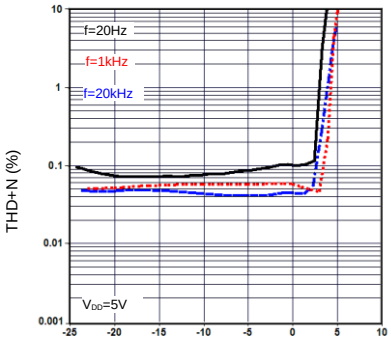
2.5V ELECTRICAL CHARACTERISTICS

(Ta=25℃;V_{DD}=2.5V, V_{SS}=0V; C_{REF} = 1uF ; R_L=32 Ω ; refer to the application circuit; unless otherwise specified)

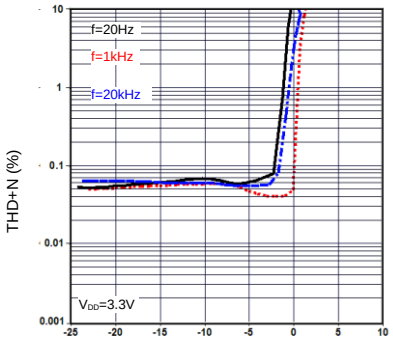
Symbol	Parameter	Conditions	Min	Typ	Max	Unit
Supply						
I_Q	Quiescent Current	V_{IN}=0V	-	4.6	-	mA
I_{PD}	Power down current	V_{IN}=0V	-	60	-	uA
PSRR	Power Supply Rejection Ratio	f = 100Hz	60	65	-	dB
General						
VO_{MAX}	Maximum Output Voltage Swing	(THD+N)/S <0.1%	-	0.707	-	Vrms
THD+N	Total Harmonic Distortion Plus Noise	V_{OUT} = 0.707Vrms	-	-65	-	dB
			-	0.056	-	%
S/N	Signal-to-Noise Ratio	V_{OUT} = 0.707Vrms	-	90	-	dB

TYPICAL PERFORMANCE CHARACTERISTICS

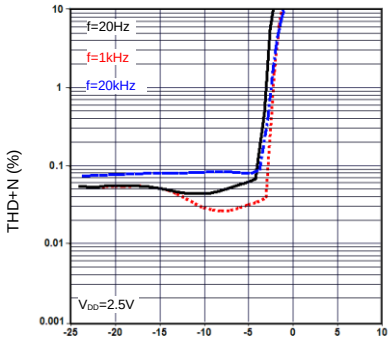
(Ta=25℃; R_L= 32Ω; C_{REF}=1uF ; refer to the application circuit ; unless otherwise specified)



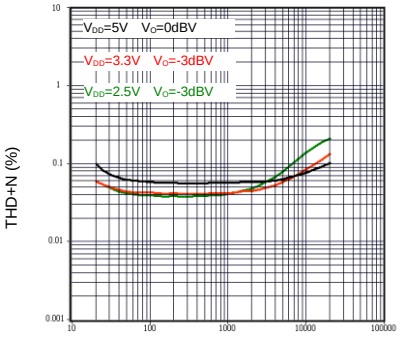
THD+N vs. output voltage



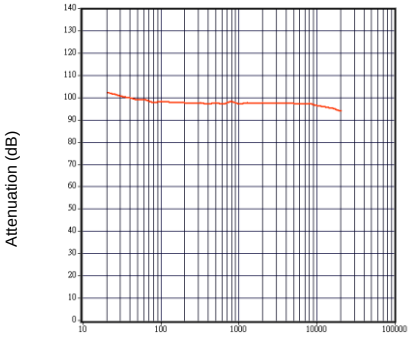
THD+N vs. output voltage



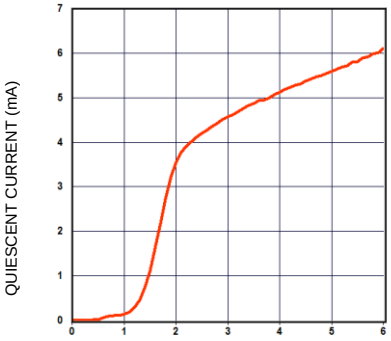
THD+N vs. output voltage



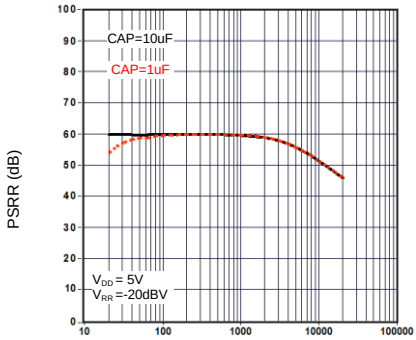
THD+N vs. frequency



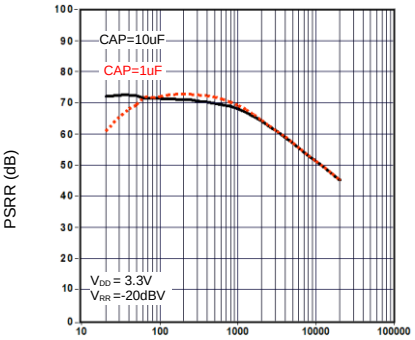
Mute vs. frequency



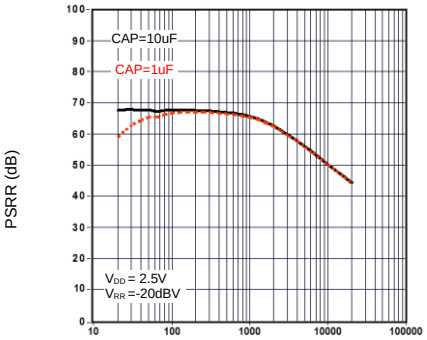
Quiescent current vs. supply voltage



PSRR vs. frequency



PSRR vs. frequency

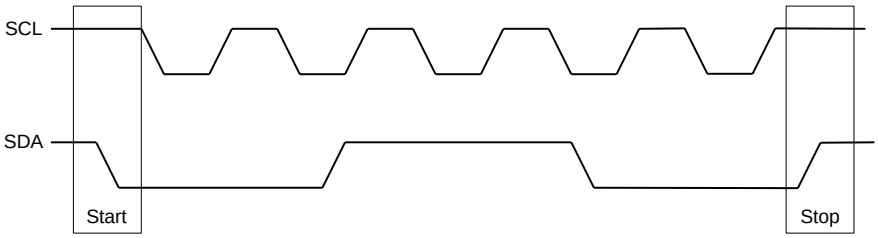


PSRR vs. frequency

I²C BUS DESCRIPTION

Start and stop conditions

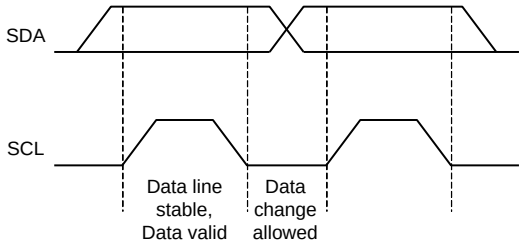
A start condition is activated when the SCL is set to HIGH and SDA shifts from HIGH to LOW state. The stop condition is activated when SCL is set to HIGH and SDA shifts from LOW to HIGH state. Please refer to the timing diagram below.



SCL : Serial Clock Line, SDA : Serial Data Line

Data validity

A data on the SDA line is considered valid and stable only when the SCL signal is in HIGH state. The HIGH and LOW states of the SDA line can only change when the SCL signal is LOW. Please refer to the figure below.

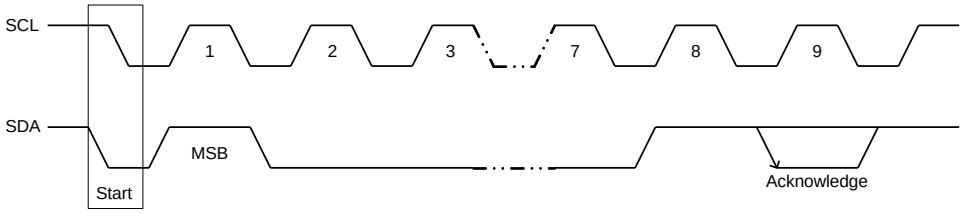


Byte format

Every byte transmitted to the SDA line consists of 8 bits. Each byte must be followed by an acknowledge bit. The MSB is transmitted first.

Acknowledge

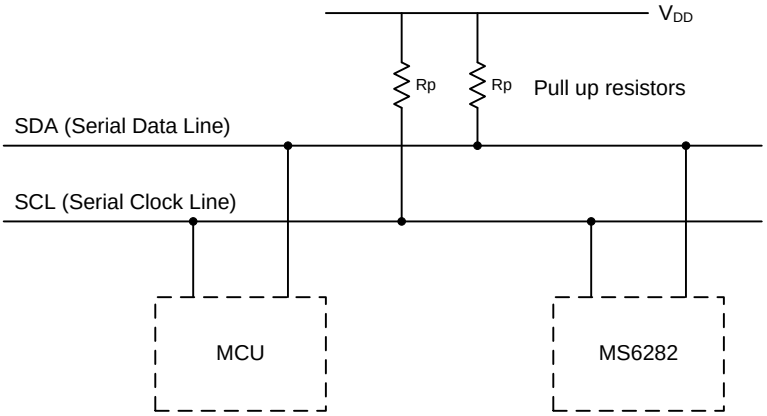
During the Acknowledge clock pulse, the master (up) put a resistive HIGH level on the SDA line. The peripheral (audio processor) that acknowledges has to pull-down (LOW) the SDA line during the Acknowledge clock pulse so that the SDA line is in a stable LOW state during this clock pulse. Please refer to the diagram below.



The audio processor that has been addressed has to generate an Acknowledge after receiving each byte, otherwise, the SDA line will remain at the HIGH level during the ninth (9th) clock pulse. In this case, the master transmitter can generate the STOP information in order to abort the transfer.

BUS INTERFACE

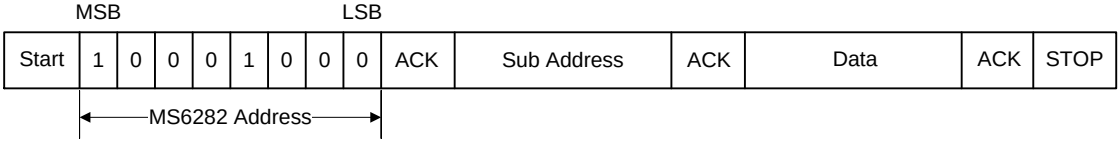
Data are transmitted to and from the MCU to the MS6282 via the SDA and SCL. The SDA and SCL make up the BUS interface. It should be noted that pull-up resistors must be connected to the positive supply voltage.



Interface protocol

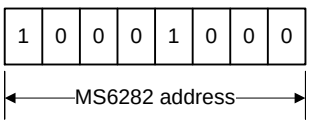
The format consists of the following

- A START condition
- A chip address byte including the MS6282 address. (7bits)
- The 8th bit of the byte must be "0".
- The MS6282 must always acknowledge the end of each transmitted byte.
- A data sequence (N-bytes + Acknowledge)
- A STOP condition



Chip Address

The chip address of the MS6282 is 88H.



SubAddress

MSB							LSB	Function
A7	A6	A5	A4	A3	A2	A1	A0	
0	0	0	0	0	0	0	0	Soft-step time / ON/OFF
0	0	0	0	0	0	0	1	L-channel Input Gain Control
0	0	0	0	0	0	1	0	R-channel Input Gain Control
0	0	0	0	0	0	1	1	Both channels Input Gain Control
0	0	0	0	0	1	0	0	L-channel Volume Control
0	0	0	0	0	1	0	1	R-channel Volume Control
0	0	0	0	0	1	1	0	Both channels Volume Control
0	0	0	0	0	1	1	1	Power management

Soft-step time / ON / OFF (0H)

MSB							LSB	Function
D7	**D6**	**D5**	**D4**	**D3**	**D2**	**D1**	**D0**	
								Soft-step Time
1					0	0	0	0.64ms
1					0	0	1	1.28ms
1					0	1	0	2.56ms
1					0	1	1	5.12ms
1					1	0	0	10.24ms
1					1	0	1	20.48ms
1					1	1	0	40.96ms
1					1	1	1	81.92ms
								Soft-step
1				0				On
1				1				Off

The initial condition is Soft-step Off, Soft-step time 40.96ms.

Input Gain Control (01H , 02H , 03H)

MSB							LSB	Function
D7	**D6**	**D5**	**D4**	**D3**	**D2**	**D1**	**D0**	**Gain**
		1	1	0	0	0	0	0dB
		1	1	0	0	0	1	1dB
		1	1	0	0	1	0	2dB
		1	1	0	0	1	1	3dB
		1	1	0	1	0	0	4dB
		1	1	0	1	0	1	5dB
		1	1	0	1	1	0	6dB
		1	1	0	1	1	1	7dB
		1	1	1	0	0	0	8dB
		1	1	1	0	0	1	9dB
		1	1	1	0	1	0	10dB
		1	1	1	0	1	1	11dB
		1	1	1	1	0	0	12dB
		1	1	1	1	0	1	13dB
		1	1	1	1	1	0	14dB
		1	1	1	1	1	1	15dB

The initial condition 14dB. We suggest the gain is set as the power is up. For example, set and fix the gain +10dB, the volume range will be controlled from -69dB to +25dB.

Volume Control (04H , 05H , 06H)

MSB							LSB	Function
D7	**D6**	**D5**	**D4**	**D3**	**D2**	**D1**	**D0**	
	0	0	0	1	1	1	1	+15dB
	0	0	0	1	1	1	0	+14dB
	:	:	:	:	:	:	:	:
	0	0	0	0	0	0	0	0dB
	0	0	1	0	0	0	0	0dB
	0	0	1	0	0	0	1	− 1dB
	:	:	:	:	:	:	:	:
	0	0	1	1	1	1	1	−15dB
	0	1	0	0	0	0	0	−16dB
	:	:	:	:	:	:	:	:
	0	1	0	1	1	1	1	−31dB
	0	1	1	0	0	0	0	−32dB
	:	:	:	:	:	:	:	:
	0	1	1	1	1	1	1	−47dB
	1	0	0	0	0	0	0	−48dB
	:	:	:	:	:	:	:	:
	1	0	0	1	1	1	1	−63dB
	1	0	1	0	0	0	0	−64dB
	:	:	:	:	:	:	:	:
	1	0	1	1	1	1	1	−79dB
	1	1	X	X	X	X	X	Mute

The initial condition is Mute.

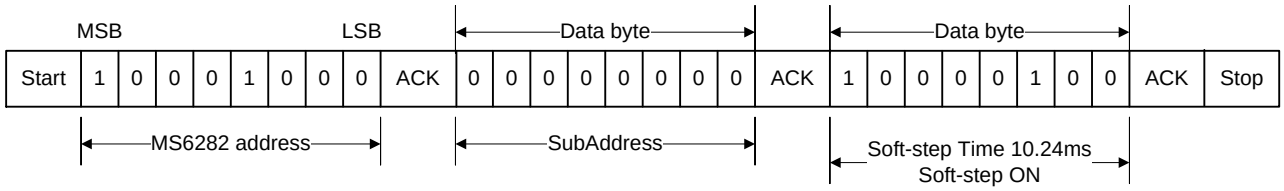
Power management (07H)

MSB							LSB	Function
D7	**D6**	**D5**	**D4**	**D3**	**D2**	**D1**	**D0**	
							0	Release of V_{REF} to GND.
							1	Set the voltage of V_{REF} to V_{DD}/2.
					0	X		All devices Active
					1	1		Power down

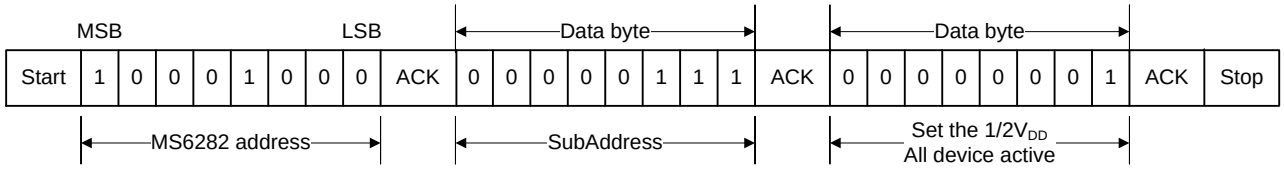
The initial condition is Power down , V_{REF} = GND.

Example

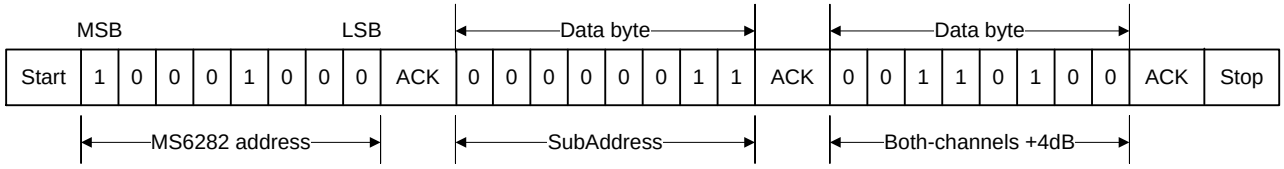
Soft-step Time 10.24ms , Soft-step ON



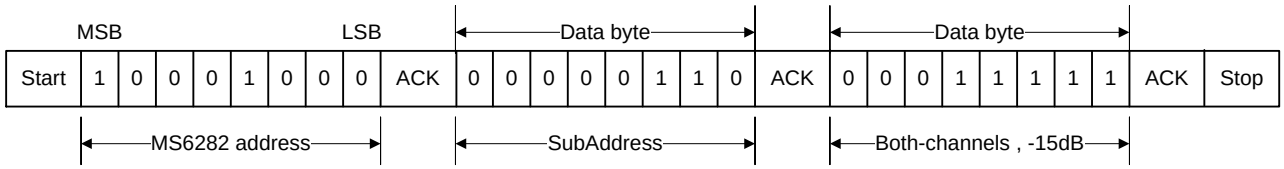
Set the 1/2V_{DD} , All device active.



Set Input gain of both channels at +4dB



Set Volume of both-channels at -15dB



Soft-step volume

When the volume-level is changed audible clicks could appear at the output. The root cause of those clicks could be the sudden change of the envelope of the audio signal. With the Soft-step feature, this click could be reduced to a minimum. Soft-step supports N dB volume change, including mute.

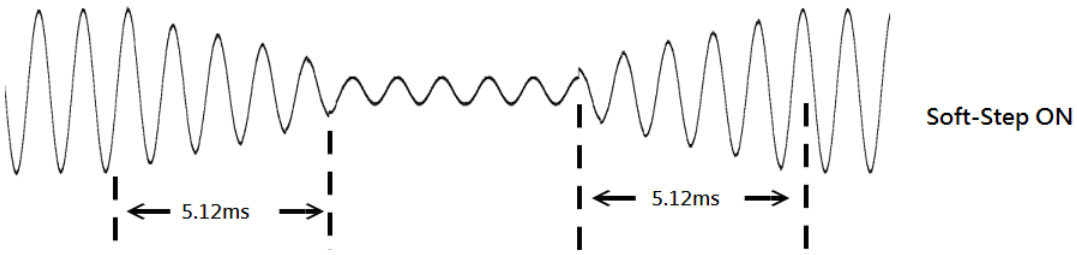
Example

Soft-Step Time = 5.12ms

0dB → -16dB → 0dB

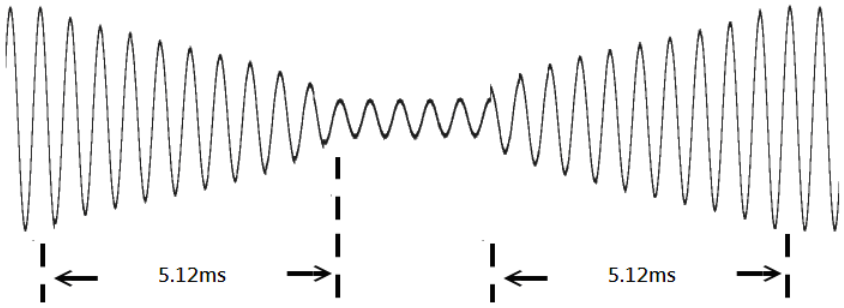
Vin = 1Vrms @ 1KHz

Soft-Step OFF



Soft-Step ON

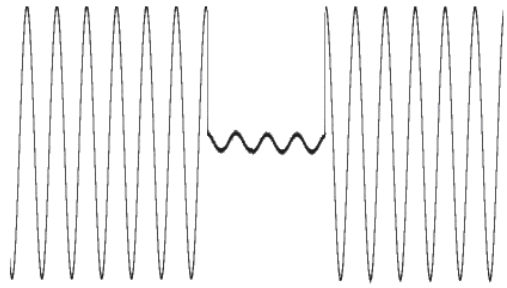
Vin = 1Vrms @ 2KHz



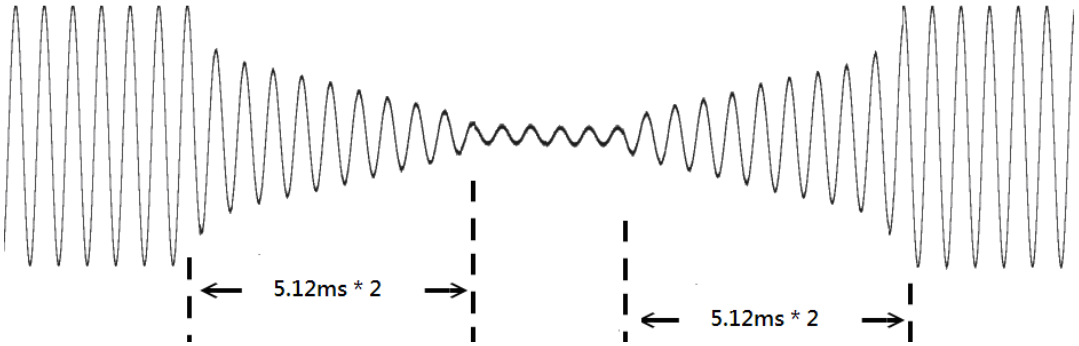
Soft-Step ON

+8dB → -16dB → +8dB

Vin = 0.5Vrms @ 1KHz



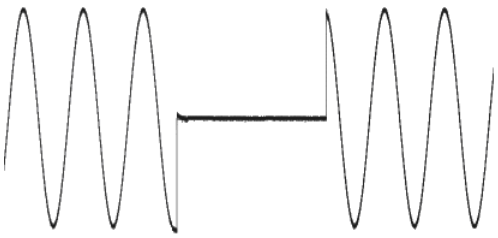
Soft-Step OFF



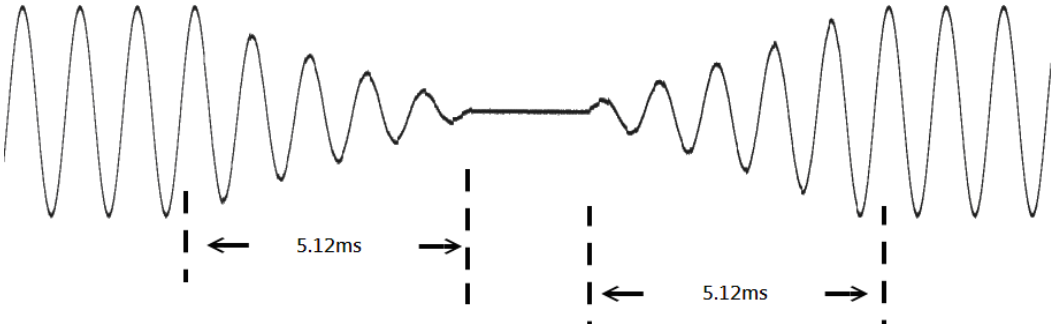
Soft-Step ON

0dB → Mute →0dB

Vin = 1Vrms @ 1KHz



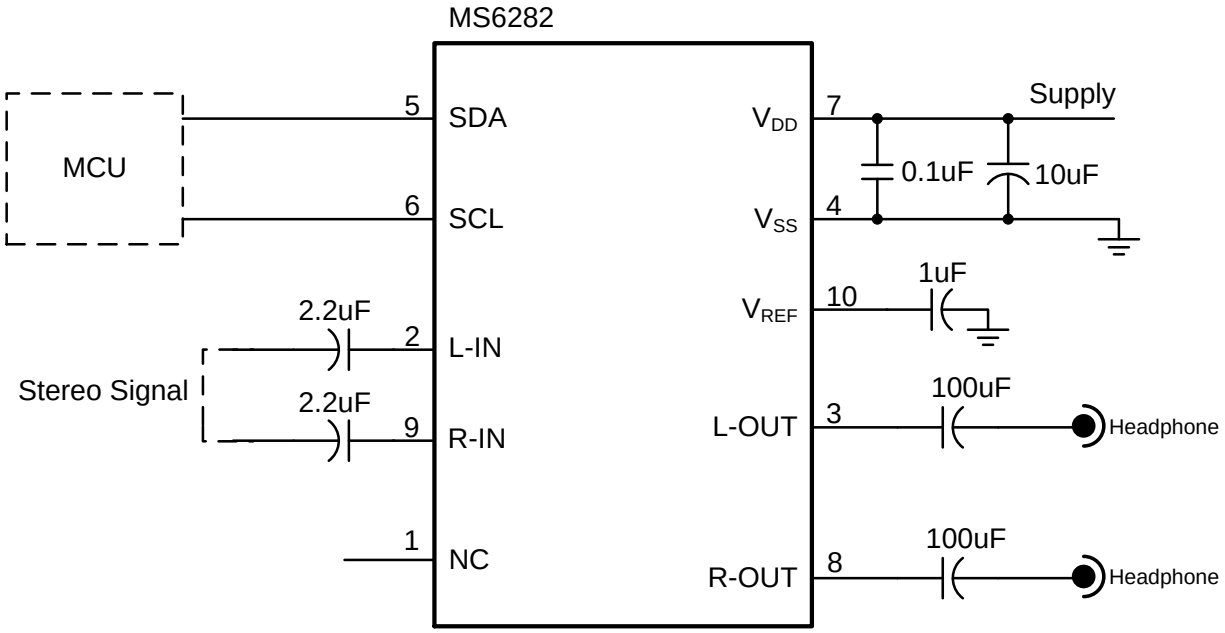
Soft-Step OFF



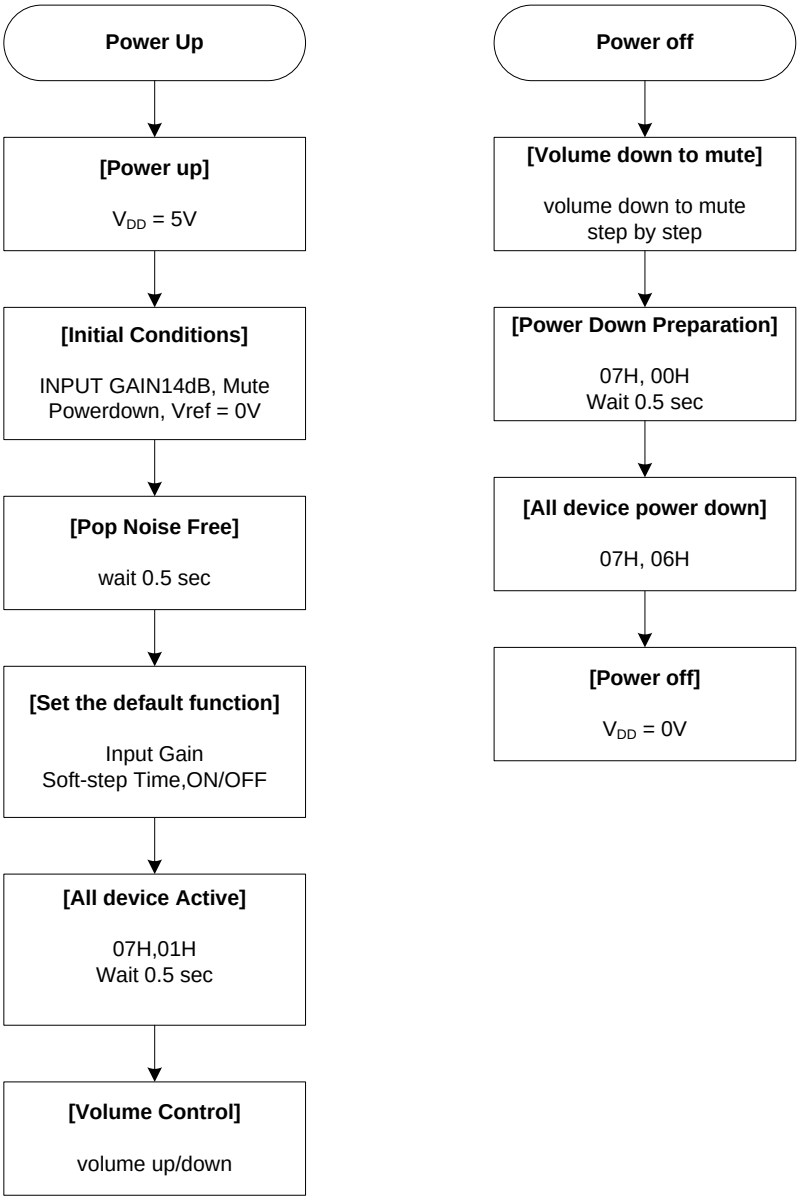
Soft-Step ON

APPLICATION INFORMATION

Basic application example

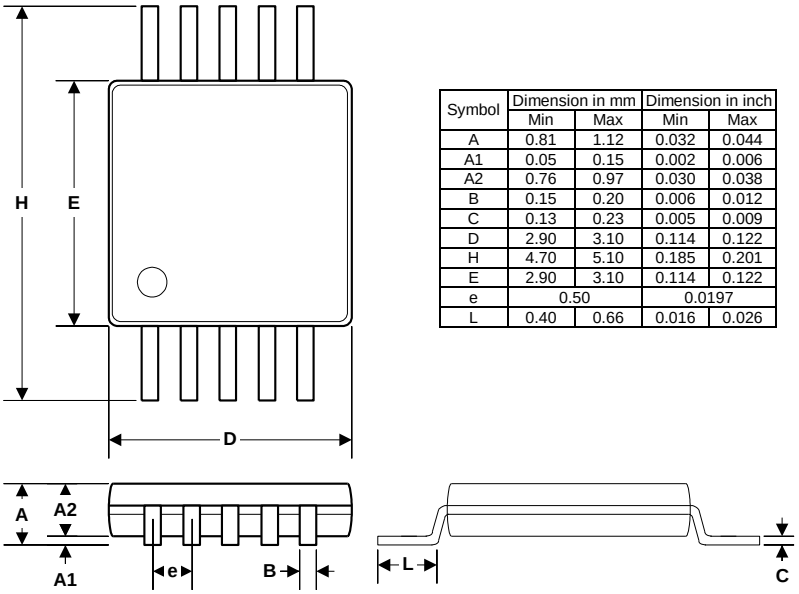


Basic application flowchart



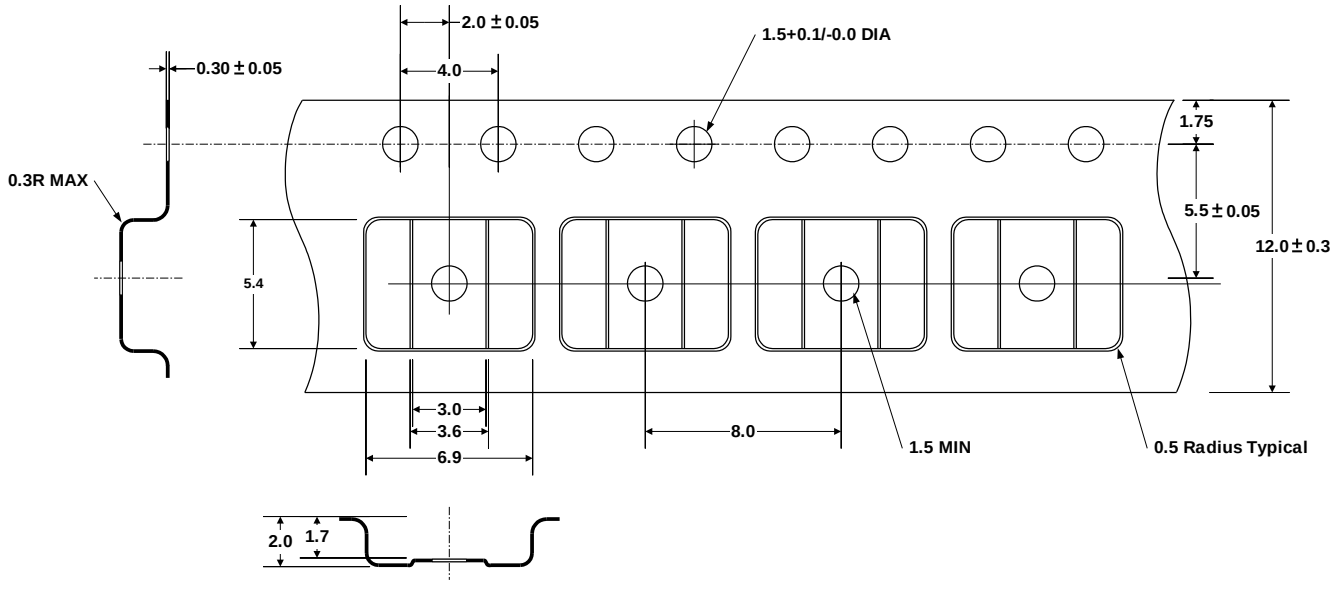
EXTERNAL DIMENSIONS

MSOP10 package



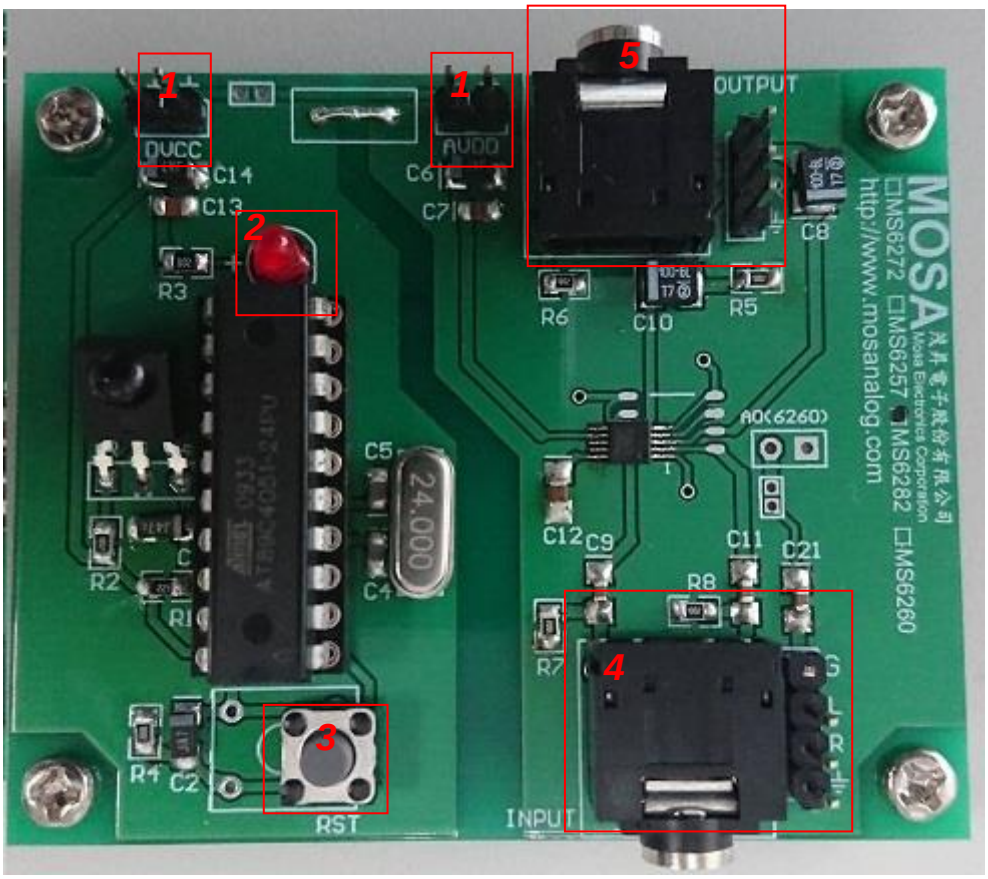
Symbol	Dimension in mm		Dimension in inch	
	Min	Max	Min	Max
A	0.81	1.12	0.032	0.044
A1	0.05	0.15	0.002	0.006
A2	0.76	0.97	0.030	0.038
B	0.15	0.20	0.006	0.012
C	0.13	0.23	0.005	0.009
D	2.90	3.10	0.114	0.122
H	4.70	5.10	0.185	0.201
E	2.90	3.10	0.114	0.122
e	0.50		0.0197	
L	0.40	0.66	0.016	0.026

TAPE AND REEL (Unit : mm)



DEMO BOARD

The demo board used IR technique controller to control the MS6282. The default states of demo board are Input Gain 0dB, Volume 0dB, Softstep on, Softstep Time 20.48ms, Mute off .



Label 1: Supply Voltage

The AVDD and DVDD should be the same supply voltage, the supply range is 2.5 ~ 6.5 VDC.

Label 2: LED Indicator

The LED indicates the power status and the IR received status. It is red-dark blink once when the MCU has received the function code correctly.

Label 3: MCU Reset

The MS6282 will be loaded the default values by MCU.

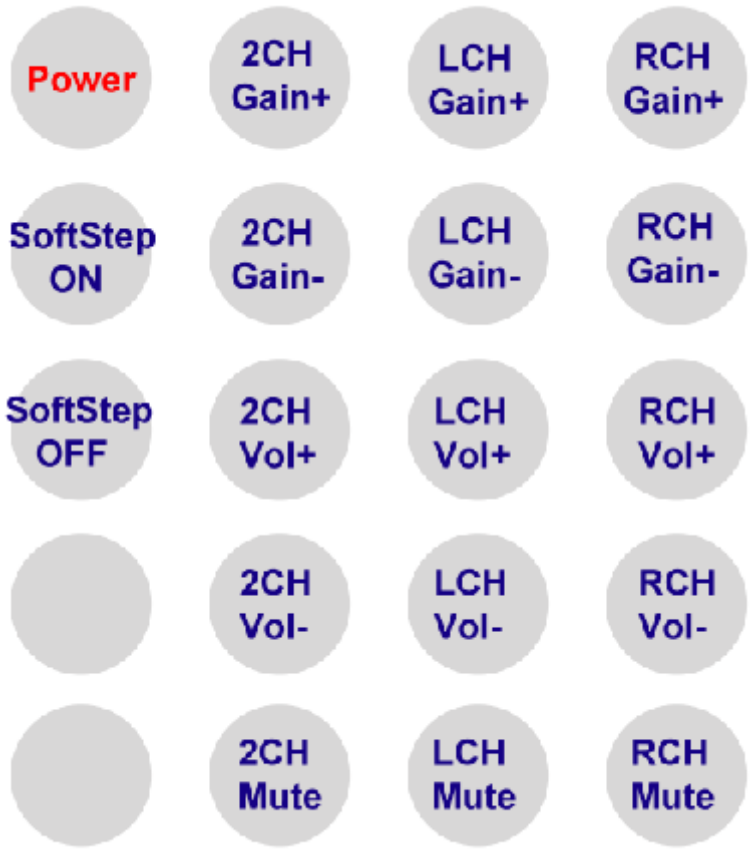
Label 4: Input Section

Please input stereo audio signal, as music or sine wave.

Label 5: Output Section

Used 3.5mm diameter of headphone.

IR Controller



Power ON/OFF : The power key.
Press the key once to set power-on or power-off for MS6282.
The default values : Input Gain 0dB, Volume 0dB, Softstep on, Softstep Time 20.48ms, Mute off .

Gain+/-：The gain control keys.

The gain control in 1dB/step as the switch is pressed once, the range is 0dB to 15dB.

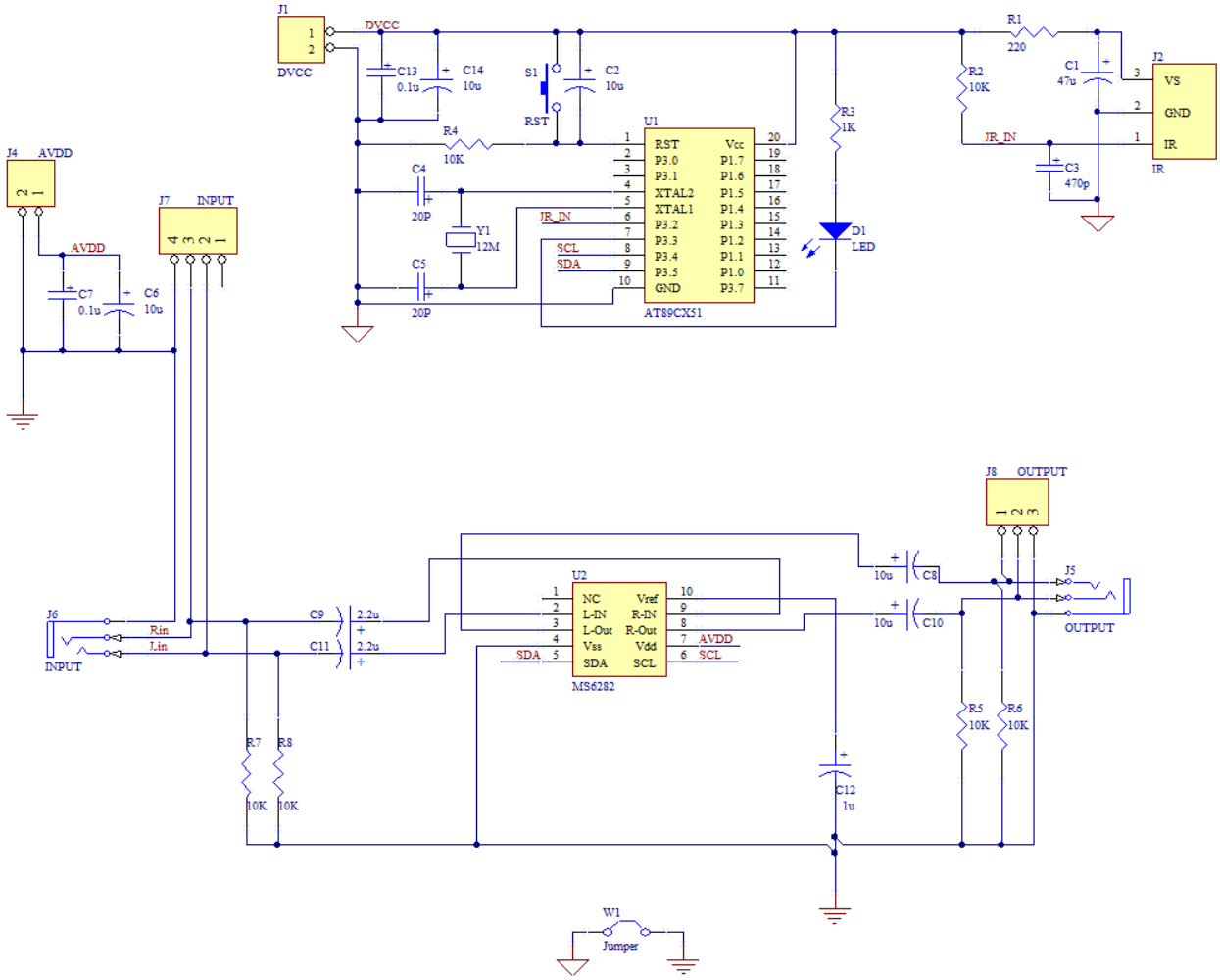
Vol+/-：The volume control keys.

The volume control in 1dB/step as the switch is pressed once, the range is –79dB to +15dB.

MUTE：The mute key controls all speaker outputs
Press the key once to set mute-on or mute-off.

SoftStep：The SoftStep key.
Press the key once to set Softstep on or SoftStep off.

Circuit


J1
DVCC
C13
0.1u
C14
10u
S1
RST
C2
10u
R4
10K
C4
20P
C5
20P
Y1
12M
U1
RST
P3.0
P3.1
XTAL2
XTAL1
P3.2
P3.3
P3.4
P3.5
GND
Vcc
P1.7
P1.6
P1.5
P1.4
P1.3
P1.2
P1.1
P1.0
P3.7
AT89CX51
JR_IN
SCL
SDA
R3
1K
D1
LED
R1
220
R2
10K
C1
47u
C3
470p
J2
VS
GND
IR
J4
AVDD
J7
INPUT
C7
0.1u
C6
10u
J8
OUTPUT
U2
NC
L-IN
L-Out
Vss
SDA
Vref
R-IN
R-Out
Vdd
SCL
AVDD
MS6282
C8
10u
C10
10u
J5
OUTPUT
J6
INPUT
Rin
Lin
C9
2.2u
C11
2.2u
R5
10K
R6
10K
R7
10K
R8
10K
C12
1u
W1
Jumper