

Low Cost 16-Bit Stereo Audio DAC

FEATURES

- Cost effective
- Low distortion
- No zero crossing distortion
- Output voltage swing is proportional to supply voltage ($V_{OPP}=V_{DD}/2$)
- Fast setting time permits 2*, 4*, and 8* oversampling (serial input) or double speed operation at 4* oversampling
- Audio Format : I2S(MS6309)
Right Justified(MS6310)
- Space saving package SOP8

APPLICATIONS

- Multimedia system
- MP3, PDA, Portable Digital Audio.
- Set top box.

DESCRIPTION

The MS6309/10 is a voltage-output Digital-to-Analog Converter (DAC). The accuracy of the weight current sources, combined with the unique symmetrical decoding method, preclude zero-crossing distortion and ensures high quality audio reproduction. the initial registers of the DAC are 0000Hex, so the DAC output will be $V_{DD}/2$.

BLOCK DIAGRAM

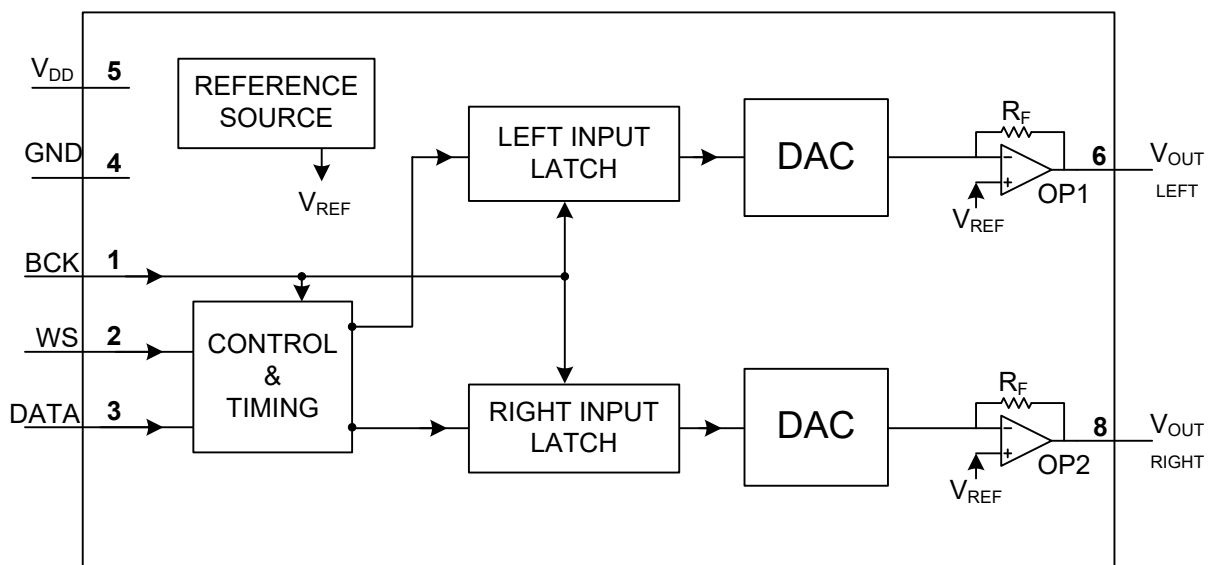
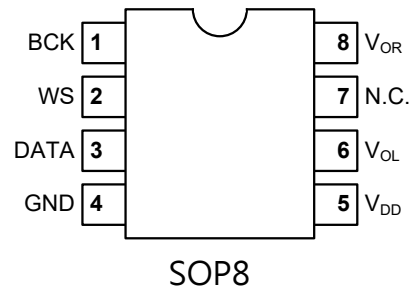


Fig.1 Block diagram.

PIN CONFIGURATION

Symbol	Pin	Description
BCK	1	Bit clock input
WS	2	Word select input
DATA	3	Data input
GND	4	Ground
V _{DD}	5	Positive supply voltage
V _{OL}	6	Left channel output
N.C.	7	not connected
V _{OR}	8	Right channel output



ORDERING INFORMATION

Package	Part number	Packaging Marking	Transport Media
8-Pin SOP (lead free)	MS6309GTR	MS6309G	2.5k Units Tape and Reel
8-Pin SOP (lead free)	MS6309GU	MS6309G	100 Units Tube
8-Pin SOP (lead free)	MS6310GTR	MS6310G	2.5k Units Tape and Reel
8-Pin SOP (lead free)	MS6310GU	MS6310G	100 Units Tube

RoHS Compliance

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Rating	Unit
V _{DD}	Positive Supply Voltage	6	V
V _{ESD}	Electrostatic Handling	-2000 to 2000	V
T _{STG}	Storage Temperature Range	-65 to 150	°C
T _A	Operating Ambient Temperature Range	-40 to 85	°C
T _J	Maximum Junction Temperature	150	°C
T _S	Soldering Temperature, 10 seconds	260	°C
R _{THJA}	Thermal Resistance from Junction to Ambient in Free Air SOP8	175	°C/W

OPERATING RATINGS

Symbol	Parameter	Min	Typ	Max	Unit
V _{DD}	Supply Voltage	2.5	-	6	V

6V ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{DD}=6V, f=1kHz ; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DC Characteristics						
V _{DC}	Output DC level		2.9	3	3.1	V
V _{FS}	Full scale output voltage	V _{FS} =0.5V _{DD}	2.8	3	3.2	V _{pp}
I _Q	Quiescent current	At code 0000H	-	3	-	mA
CS	Channel separation		85	90	-	dB
AC Characteristics						
Res	Resolution		-	-	16	bits
THD+N	Total harmonic distortion plus noise		-	-60	-54	dB
			-	0.1	0.2	%
S/N	Signal-to-noise ratio		87	93	-	dB

5V ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{DD}=5V, f=1kHz ; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DC Characteristics						
V _{DC}	Output DC level		2.4	2.5	2.6	V
V _{FS}	Full scale output voltage	V _{FS} =0.5V _{DD}	2.3	2.5	2.7	V _{pp}
I _Q	Quiescent current	At code 0000H	-	2.7	-	mA
CS	Channel separation		83	89	-	dB
AC Characteristics						
Res	Resolution		-	-	16	bits
THD+N	Total harmonic distortion plus noise		-	-60	-54	dB
			-	0.1	0.2	%
S/N	Signal-to-noise ratio		86	92	-	dB

3.3V ELECTRICAL CHARACTERISTICS

(Ta=25°C, V_{DD}=3.3V, f=1kHz ; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DC Characteristics						
V _{DC}	Output DC level		1.6	1.65	1.7	V
V _{FS}	Full scale output voltage	V _{FS} =0.5V _{DD}	1.5	1.65	1.8	V _{pp}
I _Q	Quiescent current	At code 0000H	-	2.3	-	mA
CS	Channel separation		82	88	-	dB
AC Characteristics						
THD+N	Total harmonic distortion plus noise		-	-59	-53	dB
			-	0.112	0.223	%
S/N	Signal-to-noise ratio		86	92	-	dB

2.5V ELECTRICAL CHARACTERISTICS(Ta=25°C, V_{DD}=2.5V, f=1kHz ; unless otherwise specified)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DC Characteristics						
V _{DC}	Output DC level		1.2	1.25	1.3	V
V _{FS}	Full scale output voltage	V _{FS} =0.5V _{DD}	1.2	1.25	1.5	V _{pp}
I _Q	Quiescent current	At code 0000H	-	2.0	-	mA
CS	Channel separation		80	87	-	dB
AC Characteristics						
THD+N	Total harmonic distortion plus noise		-	-58	-52	dB
			-	0.125	0.25	%
S/N	Signal-to-noise ratio		83	89	-	dB

TIMING AND DATA FORMAT

The MS6309/10 accepts input serial data formats of 16-bit word length. Left and right data words are time multiplexed. The MSB must always be first. The format of data input is shown in Figs. 2 and 3. With a HIGH level on the word select input (WS), data is placed in the left input register and with LOW level on the WS input, data is placed in the right register (Fig. 1). The data in the input registers are simultaneously latched in the output registers which control the bit switches.

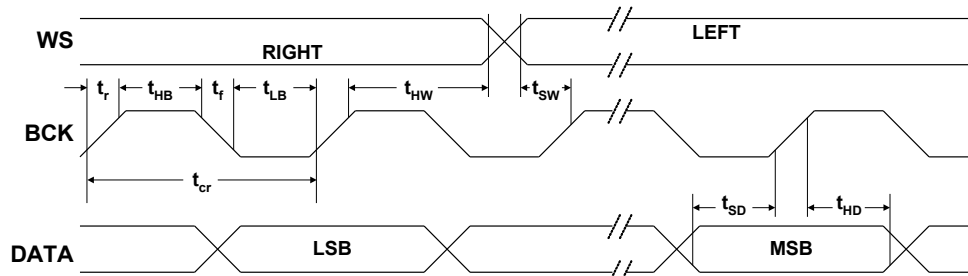


Fig.2 Timing and input signals.

Data format (BCK, WS, DATA)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
V _{IL}	Input LOW level		-	-	0.8	V
V _{IH}	Input HIGH level		2	-	-	V
I _{IL}	Input Leakage Current LOW		-	-	10	μA
I _{IH}	Input Leakage Current HIGH		-	-	10	μA
f _{BCK}	Input Clock Frequency		-	-	18.4	MHz
BR	Bit Rate Data Input		-	-	18.4	Mbits/s
f _{WS}	Word Select Input		-	-	384	kHz
t _r	Rise Time		-	-	12	ns
t _f	Fall Time		-	-	12	ns
t _{cr}	Bit Clock Cycle Time		54	-	-	ns
t _{HB}	Bit Clock High Time		15	-	-	ns
t _{LB}	Bit Clock Low Time		15	-	-	ns
t _{SD}	Data Set-up Time		12	-	-	ns
t _{HD}	Data Hold Time to Bit Clock		2	-	-	ns
t _{HW}	Word Select Hold Time		2	-	-	ns
t _{SW}	Word Select Set-up Time		12	-	-	ns

I2S format

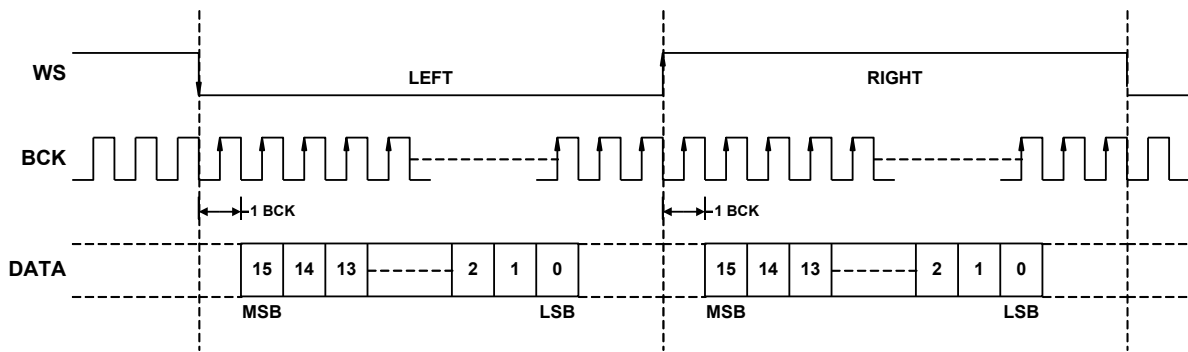


Fig.3 I2S format(MS6309)

Right justified format

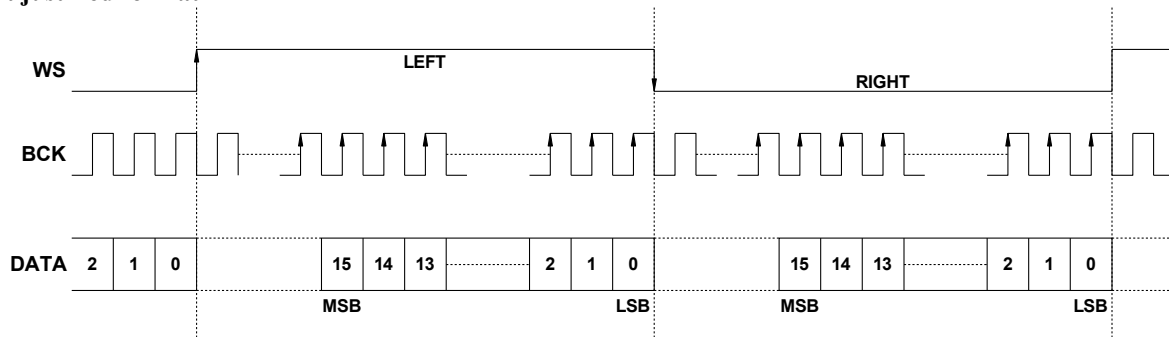


Fig.4 Right justified format(MS6310)

APPLICATION INFORMATION

Basic application example

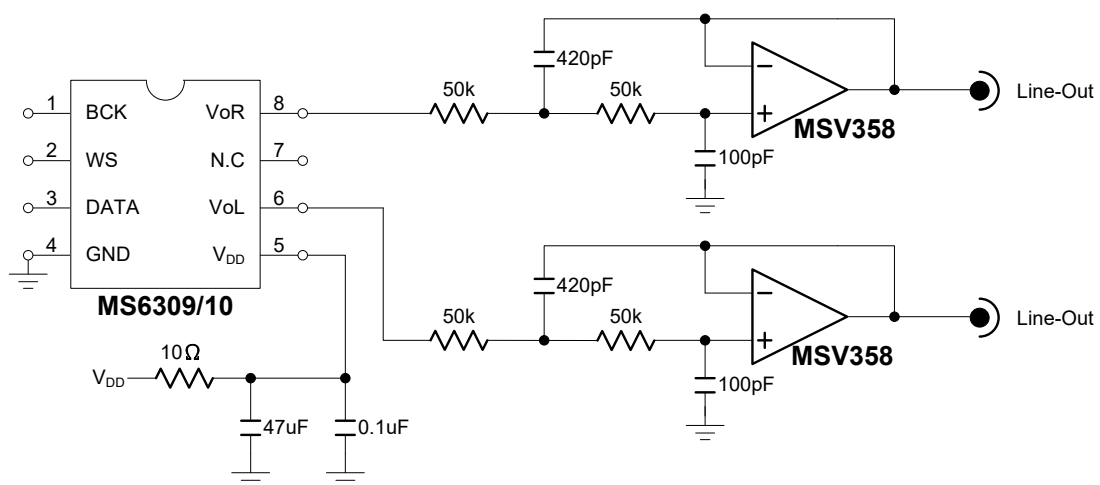
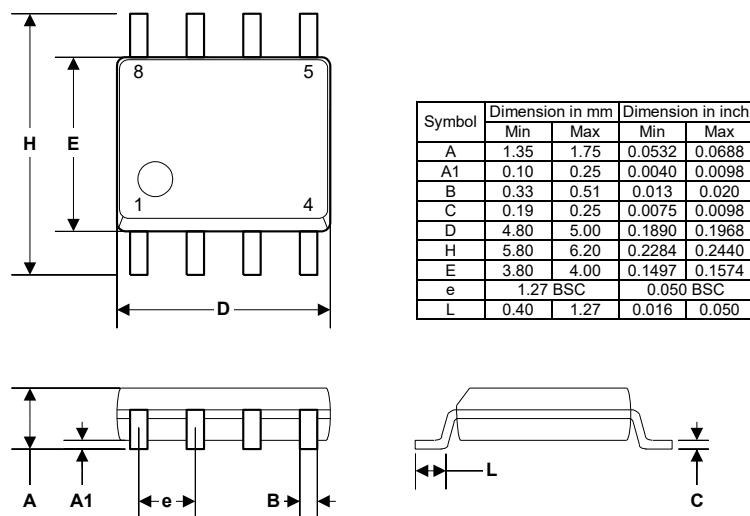


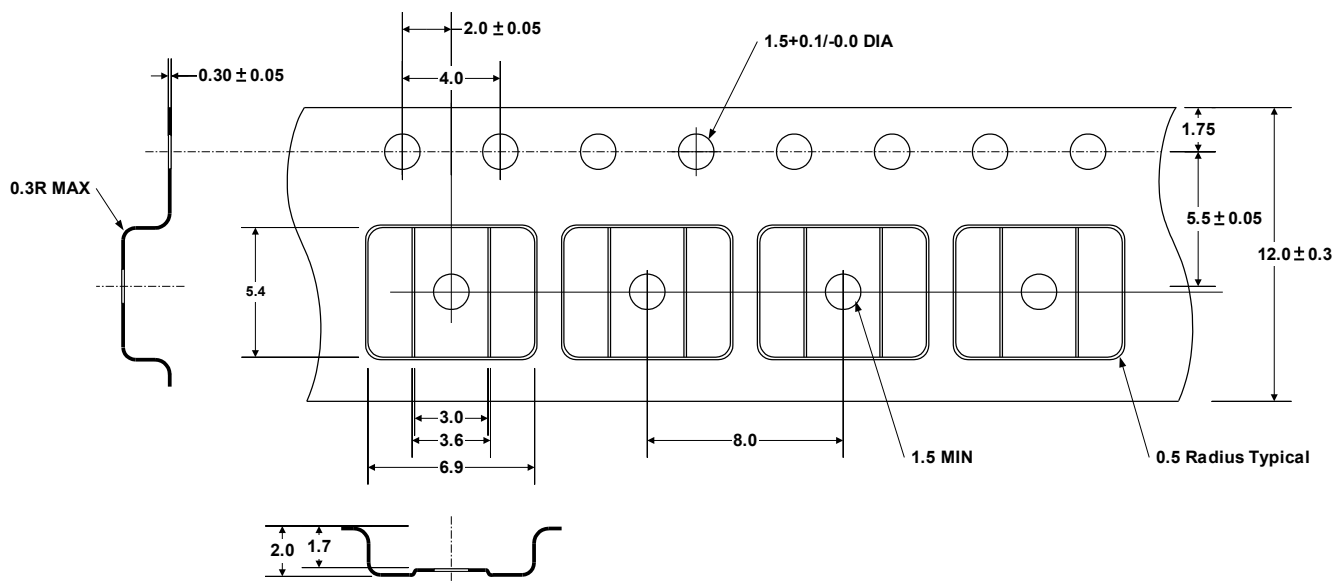
Fig.4 Example of a 2nd order filter application.

EXTERNAL DIMENSIONS



SOP8

TAPE AND REEL (Unit : mm)



SOP8